



# National Institute of Technology Durgapur

Mahatma Gandhi Avenue, Durgapur-713209

## DEPARTMENT OF ELECTRICAL ENGINEERING

**Advt. No.: NITD/SRCC/CK/EE/2023/32/ADV/01**

**Date: 01/06/2023**

A **Walk-in-Interview** will be held on **16<sup>th</sup> June, 2023 at 10:30 a.m.** in the office of the Head, Department of Electrical Engineering, NIT Durgapur for the post of Senior Research Fellows (SRFs)/ Junior Research Fellows (JRF) and Research Associates (RAs), in a collaborative R & D project with National Hydroelectric Power Corporation (NHPC), NIT Durgapur, and M/S Sensorzoid Pvt. Ltd. on "Development of Partial Discharge Monitoring Solutions for High Voltage Electrical Apparatus".

**The details of the posts are as follows;**

**A. Senior Research Fellows (JRF/SRFs) [One/Two positions].**

- 1. Required Qualification:** B.E./B. Tech with GATE qualification or M.E./M.Tech in Electrical Engineering, Electronics Engg., Computer Science, or equivalent and minimum two years of research experience (for SRF). The desirable areas of research experience are analog digital circuit, FPGA, embedded system, C/C+, Python, etc.
- 2. Fellowship Amount/Month:** Rs. 31000/- Rs. 35,000/- (JRF/SRF)
- 3. In campus accommodation** can be provided based on the availability and as per institute norms.

**B. Research Associate (RA) [Two Positions].**

- 1. Required Qualification:** PhD degree in the relevant area of research (Electrical Engineering, Electronics Engg., Computer Science,) or three years of research, teaching, and design & development experience after ME/M Tech with at least one research paper in Journal. The desirable areas of research experience are FPGA based system design, Verilog/VHDL programming.
- 2. Fellowship Amount /Month:** Rs. 47,000 – RS. 54,000/- (based on the recommendation of the selection committee).
- 3. In campus accommodation** can be provided based on the availability and as per institute norms.

**Reporting Time:** 9:30 a.m. on 16<sup>th</sup> June, 2023.

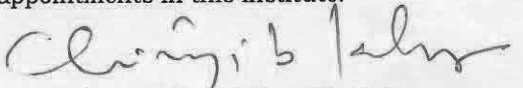
**Venue:** Office of the head, Electrical Engineering Department, NIT Durgapur

Interested candidates may send the application form in the given format, to the Principal Investigator, through *e-mail*: [chiranjib.koley@ee.nitdgp.ac.in](mailto:chiranjib.koley@ee.nitdgp.ac.in), on or before **14/06/2023**, for prior interview schedule.

The candidates must be present with the complete application form (as given below), all original certificates and results of qualified exams, DOB, experience certificate, training certificate etc. along with one set of self-attested Xerox copy for the same, BE/BTech, and ME/MTech project reports/training reports/other (PhD Thesis for RA) relevant documents as given in the application forms. Experience certificate, Publications in reputed SCI journals etc.

**Terms and conditions:**

- (1) The above position is purely contractual, and is for the duration of the project only (two years).
- (2) Selected candidates will not be entitled to claim any regular appointments in this institute.
- (3) No TA/DA will be paid for attending the interview.
- (4) Canvassing in any form will disqualify the candidature.

  
01/06/23 **Dr. Chiranjib Koley**  
Principal Investigator,

**Contact Address:**

Dr. Chiranjib Koley, Professor, Electrical; Engg. Dept.,  
NIT Durgapur, M G Road, Durgapur, West Bengal, PIN 713209, Email: [chiranjib.koley@gmail.com](mailto:chiranjib.koley@gmail.com)

**APPLICATION FOR THE POST OF SENIOR RESEARCH FELLOW (JRF/SRF/RA)  
ADVT. NO.: NITD/SRCC/CK/EE/2023/32/ADV/01, DATE: 01/06/2023**

1.	Name in full (in block letters)				Self- attested photograph
2.	Father's/Gurdain's Name				
3.	Date of Birth				
	Age as on 16.06.2023		Year	Month	
4.	a) Marital Status:	b) Gender:			
5.	a) Permanent address:	b) Correspondence address:			
	Mobile No.		E-mail		
6.	Nationality:				
8.	Category (SC/ST/OBC):				
9	GATE Score:	Rank:			

10. Educational Qualification (from Matriculation onwards):

Exam. Passed	Specialization	Board/University	Passing Year	Class	Marks/CGPA

11. Details of Employments/Training/Experience: (Year Wise starting from 2023)

12. Title of the ME/M Tech Project and brief summary (100 words):

13. Title of the BE/B Tech Project and brief summary (100 words):

14. Summary of the research experience (100 words):

15. Journal or Conference Publications (If any):

16. Field of interest:

17. Extracurricular Activities:

18. Title of the PhD Thesis and brief summary (100 words)for RA (if applicable)

19. Any other relevant achievements:

*"I hereby declare that the statements made by me in / above form are true, complete and correct to best of my knowledge and belief."*

Place:

Date:

Signature of Applicant  
Name: