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NATIONAL INSTITUTE OF TECHNOLOGY, UTTARAKHAND
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WALK-IN-INTERVIEW
(Advt. No. 10/2024 Date: 07 /05/2024)

With reference to the advertisement No. 10/2024;dated 07/05/2024,Walk-In-Interview (Hybrid Mode) shall be conducted for the position of Projector Associate-I/Junior Research Fellow for the project titled *"VLSI Design and Implementation of Imagined Speech based Neuroprosthesis: An Application in Healthcare System"* funded under Chips to Startup (C2S) Programme, MeiTY, Govt. of India under the supervision of Dr. Hariharan Muthusamy, Principal Investigator in the Department of Electronics Engineering, NIT Uttarakhand. The details of post, essential qualifications and fellowship are as under:-

Designation of post	Essential qualifications	Fellowship	Number of post	Tentative date and Time of Walk-in interview
Junior Research Fellow	M.E/M.Tech./M.S in Microelectronics and VLSI Design/Communication Systems/Biomedical Engineering/Digital System and Signal Processing/Instrumentation and Signal Processing/ Signal Processing and Control/Electronics and Communication Engineering/Communication and Signal Processing/Digital Signal Processing/ Signal Processing or allied areas with minimum of 1st class in B.E/B.Tech & M.E/M.Tech./M.S In addition GATE/NET-JRF qualification is mandatory. **Age:** Preferably below 35 Years	Rs. 37,000/- pm + HRA as per norms		
	OR			**Walk-in interview** 27/05/2024 (Monday) at 03.30 PM **Reporting Time** 02.00 PM to 03.30 PM
Project Associate – I	B.E/B. Tech in Electronics and Communication Engineering/Computer Science and Engineering/Electrical and Electronics Engineering/ Biomedical Engineering or allied areas In addition GATE/NET-JRF qualification is mandatory. **Age:** Preferably below 35 Years	Rs. 31,000/- pm + HRA as per norms	01	
	OR			
Project Associate – I	B.E/B. Tech in Electronics and Communication Engineering/Computer Science and Engineering/Electrical and Electronics Engineering/ Biomedical Engineering or allied areas **Age:** Preferably below 35 Years	Rs. 25,000/- pm + HRA as per norms		

Skill requirements

➢ Knowledge of Custom/ASIC/FPGA based VLSI design and EDA tools
➢ Knowledge of MATLAB, Hardware Description Language and Python programming
➢ Good Technical and Communication Skills.

Interested candidates shall appear for walk-in interview along with application form, all originals and self-attested photo-copies of relevant certificates as per the above schedule at Committee Room (C106), Department of Electronics Engineering, NIT Uttarakhand, Temporary Campus - Govt. ITI, Srinagar Garhwal - 246174. Candidates are urged to send the advance copy of completely filled application form and relevant certificates via email to **hariharanm@nituk.ac.in** in the subject line please specify **"Application for JRF/ Project Associate – I position in C2S project"**

General Terms & Conditions:

1. The duration of post will be initially for one year and which may be extended upto the duration of project based on the satisfactory performance. The fellowship may be terminated if performance of candidate is not satisfactory.

2. The post created under the C2S Programme, MeiTY project is purely on temporary basis and neither MeiTY nor the Institute would have liability whatsoever for sustaining temporary staff recruited under the above mentioned project, after its completion/termination.

3. Only those candidates who are present before the reporting time on scheduled date will be considered for Interview. **Candidates reporting after the scheduled reporting time for document verification shall NOT be eligible to appear for the Interview.**

4. The Junior Research Fellow will have option to register for Ph.D. program of institute; the candidate must meet the essential qualifications and qualify written examination & interview of Ph.D. admission of institute.

5. NIT Uttarakhand has right to cancel the recruitment for the aforesaid post at any time without providing any clarifications/information.

6. No interim correspondence will be entertained and canvassing in any form will lead to disqualification.

7. No TA/DA will be paid for attending the Walk-In-Interview.

-Sd-
Registrar