

**INDIAN INSTITUTE OF TECHNOLOGY, ROORKEE**  
**Electronics & Communication Engineering Department**

Dated:

**ADVERTISEMENT TO FILL UP PROJECT POSITIONS\***

Applications are invited from Indian nationals only for project position(s) as per the details given below for the consultancy/research project(s) under the Principal investigator (Name: **Prof. Anand Bulusu.**), Dept./Centre **ECE Dept** Indian Institute of Technology, Roorkee.

1. Title of project: **Development of a Stability & Reliability Aware Design Methods for SRAM Based CIM for AI Applications**
2. Sponsor of the project : Semiconductor Research Corporation, USA (Primary Collaborator Texas Instruments)
3. Project position(s) and number : Project Associate One (1)
4. Qualifications : B.E/B.Tech in ECE or equivalent (Preferably, M.E/M.Tech Microelectronics/VLSI or equivalent)
5. Emoluments : Rs. 40,000/- + HRA (as per institute rules)
6. Duration : One Year
  - Extendible till the end of the project based on periodic review after six months.
7. Job description: Job description The candidate should have a good knowledge of semiconductor device physics, network theory and VLSI circuit design. He/She should have an exposure to CMOS design logic.
  - EDA/Tools: Cadence tools, Spice, Layout-Editors, TCAD device/process simulation, TCAD mixed-mode simulation.
  - Hardware Descriptive language: Verilog or Verilog-A (desirable)
  - Scripting Language : Python, Perl or Bash
8. Mandatory Industry internship on the project topic with collaborating industry.
9. The candidate should have enthusiasm to coordinate and work at pace with collaborating industry.
10. Candidates before appearing for the interview shall ensure that they are eligible for the position they intend to apply.
11. Candidates desiring to appear for the Interview should submit their applications with the following documents to the office of Principal Investigator through email, ([vlsi-office@ece.iitr.ac.in](mailto:vlsi-office@ece.iitr.ac.in))
  - Application in a plain paper with detailed CV including chronological discipline of degree/certificates obtained.
  - Experience including research, industrial field and others.
  - Attested copies of degree/certificate and experience certificate.
  - **Subject of the mail should be "Application for the post of JRF in a project entitled (project name)"**
12. Candidate shall bring along with them the original degree(s)/certificate(s) and experience certificate(s) at the time of interview for verification.
13. Preference will be given to SC/ST candidates on equal qualifications and experience.
14. Please note that no TA/DA is admissible for attending the interview.

**Note: The selected candidate may get an opportunity for PhD admission. (GATE mandatory for Ph.D.)**  
The last date for application to be submitted to office of Principal Investigator is **April 12, 2024**  
The date of online interview shall be informed to the shortlisted candidates by email.

*B. Anand*

Tel:+91-1332-285347  
Mob No - 8794862482  
Email:vlsi-office@ece.iitr.ac.in

Fax:

Name and signature  
of Principal Investigator

\*To be uploaded on IIT Roorkee website and copy may be sent to appropriate addresses by PI for wider circulation.

*Approved*

*18/3/24*  
*16/3/24*  
डीन (सिक)/(Dean SRIC)  
भा. प्रौ. सं. रुड़की/IIT Roorkee